

DEC 29 2006

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**FACSIMILE COVER LETTER**

**To:** Commissioner for Patents  
Examiner Jesse A. Fenty

**Firm:** U.S. Patent and Trademark Office  
Art Unit 2815

**Facsimile:** (571) 273-8300

**From:** Grace L. Pan

**Date:** December 29, 2006

**Re:** FLH Ref No.: 790001-2042  
Serial No: 10/752,642

**Number of Pages:** 8  
(including cover page)

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00419829

U.S. Application No. 10/752,642  
Reply to Office Action mailed November 2, 2006

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**PATENT**  
**790001-2042**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s) : Naofumi Nakamura et al.  
Serial No. : 10/752,642  
For : SEMICONDUCTOR DEVICE AND METHOD OF  
FABRICATING THE SAME  
Filing Date : January 7, 2004  
Examiner : Jesse A. Fenty  
Group Art Unit : 2815  
Confirmation No. : 4109

**FACSIMILE**

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BERNADETTE VASSALLO

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Signature

December 29, 2006

Date of Signature

745 Fifth Avenue  
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**RESPONSE**

**MAIL STOP AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This response is submitted in reply to the Final Office Action in the above-captioned  
application mailed on November 2, 2006, having a three-month statutory period for response set  
to expire on February 2, 2007, Applicants respond as follows.